

Programme

Monday 10th September

10.30	Arrival/Coffee	
11.00	Welcome	Bill Roscoe and Michael Goldsmith
11.15	Jim Davies	Automatic Refinement of Transactional Information Systems
12.15	Tomasz Mazur and Gavin Lowe	Counter Abstraction in the CSP/FDR setting
12.55	Lunch	
14.00	Adam Antonik and Michael Huth	On the complexity of semantic self-minimization
14.40	Cas Cremers and Pascal Lafourcade	Comparing State Spaces in Automatic Security Protocol Verification
15.20	Jeremy Dawson	Isabelle Theories for Machine Words
16.00	Tea	
16.30	Steve Schneider, Helen Treharne and Beeta Vajar	Introducing mobility into CSP B
16.50	Michael Goldsmith	FDR Hints and Tips
17.50	End	
18.15	Oxford Walk	

Tuesday 11th September

9.00	Jakob Rehof	From CSP to CCS and back: Unified theories of compositional deadlock-preserving refinement.
10.00	Joel Henry, Jane Bao and Dylan Flaherty	A Set Based Method for Constraint Determination in C Programs
10.40	Coffee	
11.10	Jan Jürjens	Automated Security Verification for Crypto Protocol Implementations: Verifying the Jessie Project
11.50	Mohammad Izadi and Ali Movaghar	Compositional Failure-based Equivalence of Constraint Automata
12.30	Gavin Lowe	On CSP refinement tests that run multiple copies of a process
13.15	Lunch	
14.15	Muffy Calder	Verification challenges of computational systems biology
15.15	Paolo Ballarini and Andras Horvath	Compositional Model Checking of product-form CTMCs
15.50	Tea	
16.20	Marc Fontaine and Michael Leuschel	Typechecking CSP Specifications using Haskell
16.40	David Hopkins and Bill Roscoe	SVA: a tool for analysing shared-variable programs
17.00	David Parker	Probabilistic Model Checking
18.00	End	
19.30	Dinner at Worcester College	

Wednesday 12th September

9.00	Antti Valmari	Cut States and Elusive Actions
10.00	Rahul Kumar, Eric Mercer and Annette Bunker	Improving Translation of Live Sequence Charts to Temporal Logic
10.40	Coffee	
11.10	Harald Fecher, Michael Huth, Heiko Schmidt and Jens Schoenborn	Refinement sensitive formal semantics of state machines with persistent choice
11.50	Douglas Graham, Muffy Calder and Alice Miller	An Inductive Technique for Parameterised Model Checking of Degenerative Distributed Randomised Protocols
12.30	Lyes Benalycherif and Anthony McIsaac	A Semantic Condition for Data Independence and Applications in Hardware Verification
13.15	Lunch	
14.15	Michael Leuschel and Daniel Plagge	Seven at one stroke: LTL model checking for B, Z, CSP, and more
14.45	Bill Roscoe	Verifying Buffered Systems
15.45	Close	
16.00	Tea/Departure	