Dear Mike,

You asked me to put in writing my comments on your draft proposal for BS\textsuperscript{c} honours in Computer Science.

I very much like the first sentence of the philosophy. But I would continue:

"The computer scientist uses the technique of levels of abstraction to master the complexity of his subject and its applications. The subject itself is studied at many levels of abstraction, and the computer scientist must understand them, and the relations between them. At the same time, it is a practical subject, and at all levels the computer scientist must be able to translate..."
his understanding into skills in inventing, design, verification, implementation of useful computer systems.

The basic levels at which the subject are studied are:

1. Digital Logic.
3. Programming in a low level language.
4. Programming in a high level language.

The bridging subjects between these levels are:

1)-(2) VLSI chip design
2)-(3) Assembler & basic software
3)-(4) Computer Construction
4)-(5) Program design and verification.
5)-(6) Formal language definition.
At the same time, certain important application areas must be studied:

2. Commercial Data Processing including systems analysis.

On CSIA, I would emphasise the skills which I wish to impart: e.g. 1) To design and write PASCAL programs up to 120 (?) lines.
   2) To read, understand, and modify well-documented PASCAL programs up to 600 (?) lines.
   3) To understand and use effectively the available simple programming tools.
Similarly, for unit 3, where it seems that the storage management could well be omitted.

Similarly for the course in Discrete mathematics, which should have as its objective that the student should be able to prove theorems using algebraic identities and natural or structural induction; and that he should be able rapidly to find counterexamples for proposed theorems which are false. For this reason I would include predicate logic in the syllabus. The "size" of the proof should be limited to about ten lines; but he should be able to read and check proofs up to (say) thirty lines.
I would combine courses (b), (c), (d) into a single course with a practical orientation towards VLSI design.

In general, it is administratively very convenient to introduce a standard sized module for your course (say 36 lectures). Then each course can have a single examination set by and marked by a single examiner. You will also find that your syllabus is much more flexible, and you can rapidly develop and improve the syllabus. Within each standard module, the lecturer team has considerable freedom of improvement; and it is much easier to drop old courses and to try out new ones.
Furthermore, particularly for the more advanced and application-oriented courses, you will probably want to offer some choice. It's surprising how tastes differ, e.g. between those who are excited by the theory, and those who take to commercial data processing.

Thanks again for your hospitality and good luck in your course development.

Yours sincerely,
Tony